

JEE 2600

Introduction to Digital Logic and Computer Design

Course Information

- Professor: Tom Jolley
jolley@seas.wustl.edu
- Text: Digital Design with RTL Design, VHDL, and Verilog
- Website:
<http://classes.engineering.wustl.edu/jee2600>
- Syllabus: On website

Class Procedures

- Email if you miss class
- Email if you have questions
- Read the book
- Do the homework
- Work with your classmates (except on quizzes or exams)

Homework

- The assigned homework is the minimum amount of problems you should work.
- Will not be collected (you can still send it to the teacher)
- Posted by section
- Work with classmates
- Solutions posted about the same time as questions

Topics Covered

- Boolean Algebra, number systems,
- Combinational logic
- Sequential logic
- Register Transfer Level (RTL)
- Memory
- Programmable Processors (an introduction)

Resources

- Academic Integrity: Very important
- Lecture Notes: Current semester and previous semester
- The Author's website:
<http://www.ddvahid.com>