

**Spring Semester 2021**

**CSE463M-563M: Digital Integrated Circuit Design and Architecture**

**Homework #1**

**CSE 463M and CSE 563M students:**

**1) Solve problems (10 points each)**

**3.1, 3.2, 3.3, 3.6, 3.10, 3.12.**

**from Chapter 3 from the textbook. Please show all your work and be neat and circle your answers.**

**2) Cadence exercise (20 points):**

**Repeat Transistor I-V characteristics exercise from the Cadence wiki. Print out and submit**

**Transistor schematics and I-V graph just like it is done in the Cadence wiki. To access**

**Cadence you need to login to a Linux Machine at**

[**https://linuxlab.engr.wustl.edu**](https://linuxlab.engr.wustl.edu)

**Once you login to the Linux Machine start running Cadence exercise.**

**CSE 563M students only:**

**In addition to the problem set above, please solve the following problem from the textbook: 3.9 (10 points).**