

Data Sheet report:

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All values displayed in nanoseconds (ns)

Setup/Hold to clock clk

Source	Max Setup to clk (edge)	Process Corner	Max Hold to clk (edge)	Process Corner	Internal Clock(s)	Clock Phase
d<0>	0.733(R)	FAST	0.246(R)	SLOW	clk_BUF	0.000
d<1>	0.920(R)	FAST	0.091(R)	SLOW	clk_BUF	0.000
d<2>	0.872(R)	FAST	0.153(R)	SLOW	clk_BUF	0.000
d<3>	0.617(R)	FAST	0.322(R)	SLOW	clk_BUF	0.000
d<4>	0.199(R)	FAST	0.879(R)	SLOW	clk_BUF	0.000
d<5>	0.425(R)	FAST	0.589(R)	SLOW	clk_BUF	0.000
d<6>	0.786(R)	FAST	0.265(R)	SLOW	clk_BUF	0.000
d<7>	0.795(R)	FAST	0.198(R)	SLOW	clk_BUF	0.000
d<8>	0.955(R)	FAST	0.002(R)	SLOW	clk_BUF	0.000
d<9>	0.819(R)	FAST	0.195(R)	SLOW	clk_BUF	0.000
d<10>	0.869(R)	FAST	0.158(R)	SLOW	clk_BUF	0.000
d<11>	0.860(R)	FAST	0.147(R)	SLOW	clk_BUF	0.000
d<12>	0.890(R)	FAST	0.104(R)	SLOW	clk_BUF	0.000
d<13>	0.881(R)	FAST	0.081(R)	SLOW	clk_BUF	0.000
d<14>	0.805(R)	FAST	0.139(R)	SLOW	clk_BUF	0.000
d<15>	0.887(R)	FAST	0.101(R)	SLOW	clk_BUF	0.000
d<16>	0.851(R)	FAST	0.154(R)	SLOW	clk_BUF	0.000
d<17>	0.993(R)	FAST	0.052(R)	SLOW	clk_BUF	0.000
d<18>	0.791(R)	FAST	0.209(R)	SLOW	clk_BUF	0.000
d<19>	0.904(R)	FAST	0.066(R)	SLOW	clk_BUF	0.000
d<20>	0.968(R)	FAST	0.036(R)	SLOW	clk_BUF	0.000
d<21>	0.872(R)	FAST	0.125(R)	SLOW	clk_BUF	0.000
d<22>	1.004(R)	FAST	-0.066(R)	SLOW	clk_BUF	0.000
d<23>	1.045(R)	FAST	-0.114(R)	SLOW	clk_BUF	0.000
d<24>	0.798(R)	FAST	0.220(R)	SLOW	clk_BUF	0.000
d<25>	1.256(R)	FAST	-0.314(R)	SLOW	clk_BUF	0.000



address<21>		5.584(R)		SLOW		2.785(R)		FAST		clk_BUF		0.000	
address<22>		5.580(R)		SLOW		2.797(R)		FAST		clk_BUF		0.000	
address<23>		5.543(R)		SLOW		2.765(R)		FAST		clk_BUF		0.000	
address<24>		5.614(R)		SLOW		2.813(R)		FAST		clk_BUF		0.000	
address<25>		5.588(R)		SLOW		2.787(R)		FAST		clk_BUF		0.000	
address<26>		5.610(R)		SLOW		2.806(R)		FAST		clk_BUF		0.000	
address<27>		5.540(R)		SLOW		2.762(R)		FAST		clk_BUF		0.000	
address<28>		5.600(R)		SLOW		2.799(R)		FAST		clk_BUF		0.000	
address<29>		5.607(R)		SLOW		2.801(R)		FAST		clk_BUF		0.000	
address<30>		5.610(R)		SLOW		2.806(R)		FAST		clk_BUF		0.000	
address<31>		5.590(R)		SLOW		2.789(R)		FAST		clk_BUF		0.000	
d<0>		7.202(R)		SLOW		3.037(R)		FAST		clk_BUF		0.000	
d<1>		7.067(R)		SLOW		3.017(R)		FAST		clk_BUF		0.000	
d<2>		7.081(R)		SLOW		2.984(R)		FAST		clk_BUF		0.000	
d<3>		7.175(R)		SLOW		3.124(R)		FAST		clk_BUF		0.000	
d<4>		8.031(R)		SLOW		3.245(R)		FAST		clk_BUF		0.000	
d<5>		8.010(R)		SLOW		3.402(R)		FAST		clk_BUF		0.000	
d<6>		7.137(R)		SLOW		2.968(R)		FAST		clk_BUF		0.000	
d<7>		7.136(R)		SLOW		2.928(R)		FAST		clk_BUF		0.000	
d<8>		7.229(R)		SLOW		2.964(R)		FAST		clk_BUF		0.000	
d<9>		7.323(R)		SLOW		2.958(R)		FAST		clk_BUF		0.000	
d<10>		7.086(R)		SLOW		2.967(R)		FAST		clk_BUF		0.000	
d<11>		7.440(R)		SLOW		2.980(R)		FAST		clk_BUF		0.000	
d<12>		7.346(R)		SLOW		2.961(R)		FAST		clk_BUF		0.000	
d<13>		7.365(R)		SLOW		2.959(R)		FAST		clk_BUF		0.000	
d<14>		7.456(R)		SLOW		2.956(R)		FAST		clk_BUF		0.000	
d<15>		7.277(R)		SLOW		2.979(R)		FAST		clk_BUF		0.000	
d<16>		7.363(R)		SLOW		2.973(R)		FAST		clk_BUF		0.000	
d<17>		7.037(R)		SLOW		2.850(R)		FAST		clk_BUF		0.000	
d<18>		7.097(R)		SLOW		2.793(R)		FAST		clk_BUF		0.000	
d<19>		7.230(R)		SLOW		2.904(R)		FAST		clk_BUF		0.000	
d<20>		6.920(R)		SLOW		2.947(R)		FAST		clk_BUF		0.000	
d<21>		7.122(R)		SLOW		2.849(R)		FAST		clk_BUF		0.000	
d<22>		7.212(R)		SLOW		2.844(R)		FAST		clk_BUF		0.000	
d<23>		7.007(R)		SLOW		2.824(R)		FAST		clk_BUF		0.000	

