

**Spring Semester 2020**

**CSE463M: Digital Integrated Circuit Design and Architecture**

**Homework #1**

**1) Solve problems:**

**3.1,**

**3.2,**

**3.3,**

**3.6,**

**3.10,**

**3.12**

**from Chapter 3 from the textbook.**

**Please show all your work and be neat and circle your answers.**

**2) Cadence exercise:**

**Repeat Transistor I-V characteristics exercise from the Cadence wiki. Print out and submit**

**Transistor schematics and I-V graph just like it is done in the Cadence wiki. To access**

**Cadence you need to login to a Linux Machine at**

[**https://linuxlab.engr.wustl.edu**](https://linuxlab.engr.wustl.edu)

**Once you login to the Linux Machine start running Cadence exercise.**