

## Preliminary Schedule

Week	Date	Sections	Notes
1			
	28-Aug	Intro, motivation, 5.1	Prerequisite Survey
2	02-Sep	MOS transistor theory	Ch3
	04-Sep	MOS transistor theory	HWK#1 assigned
3	09-Sep	MOS transistor theory	
	11-Sep	MOS transistor theory	HWK#1 due, HWK#2 assigned
4	16-Sep	MOS + Cadence Tool	
	18-Sep	Inverter: Static Char.	HWK#2 due, HWK#3 assigned
5	23-Sep	Inverter: Static Char.	Ch5
	25-Sep	Inverter: Static Char.	HWK#3 due, HWK#4 assigned
6	30-Sep	Inverter: Static Char.	
	02-Oct	Midterm#1	HWK#4 due
7	07-Oct	Inverter: Switching Char.	Ch6
	09-Oct	Inverter: Switching Char.	HWK#5 assigned
8	14-Oct	Combinational Logic	Ch7
	16-Oct	Combinational Logic	HWK#5 due, HWK#6 assigned
9	21-Oct	Combinational Logic	
	23-Oct	Combinational Logic	HWK#6 due, HWK#7 assigned
10	28-Oct	Combinational Logic	
	30-Oct	Combinational Logic, review	HWK#7 due, HWK#8 assigned
11	04-Nov	Midterm#2	
	06-Nov	Sequential Logic	HWK#8 due, HWK#9 assigned
12	11-Nov	Sequential Logic	Ch8
	13-Nov	Sequential Logic	HWK#9 due
13	18-Nov	Sequential Logic	HWK#10 assigned
	20-Nov	Dynamic Logic	Ch9
14	25-Nov	Dynamic Logic	HWK#10 due
	27-Nov	<i>THANKSGIVING</i>	<i>NO CLASS</i>
15	02-Dec	Memories	Ch10, HWK#11 assigned
	04-Dec	Review for Final	
16	-	-	-
	-	-	-
	16-Dec	Final Exam	FINAL, HWK#11 due

NOTE: This is not final schedule. Schedule may change as communicated in the classroom. Please check the class website for up to date schedule.