

1. Download [SynCompile.tgz](#) from [here](#) and unzip it in the Linux Lab by following command:

```
tar -xzf SynCompile.tgz
```

You will get the following files in the folder *SynCompile*:

AN.DB	11/28/2016 8:38 PM	File folder	
VTVT_TSMC180	11/28/2016 8:38 PM	File folder	
.synopsys_vss.setup	11/28/2016 8:10 PM	SETUP File	1 KB
Bitcoin.syn.v	11/23/2016 3:44 PM	Notepad++ Docu...	6 KB
Bitcoin_tb.v	11/23/2016 3:52 PM	Notepad++ Docu...	2 KB
Makefile	11/28/2016 8:37 PM	File	1 KB
vtvt_tsmc180_Vcomponents.vhd	11/27/2015 5:01 PM	Virtual Hard Disk	94 KB
vtvt_tsmc180_VITAL.vhd	11/27/2015 5:01 PM	Virtual Hard Disk	322 KB
vtvt_tsmc180_Vtables.vhd	11/27/2015 5:01 PM	Virtual Hard Disk	4 KB

Copy your *<filename>.syn.v* and *<testben>.v* to the SynCompile folder. In the above example the two files are *Bitcoin.syn.v* and *Bitcoin\_tb.v*.

2. Modify the Makefile to import the files you just copied in.

- Change variable *synv* to *<filename>.syn.v* (In the above example it is *Bitcoin.syn.v*)
- Change variable *techbench* to *<testben>.v* (In the above example it is *Bitcoin\_tb.v*)
- Change variable *top\_tb\_module* to the *top module in testbench* (In the above example it is *twoblock\_tb*)

```
13 #=====
14 synv = Bitcoin.syn.v
15 testbench = Bitcoin_tb.v
16 top_tb_module = twoblock_tb
17
```

```
3 module twoblock_tb;
4     reg CLK;
5     reg [1023:0]x;
6     wire [255:0]y;
7
```

3. Run the following command you should get the *simv* file:

```
make
```

And run the following command you should run the *simv* file:

```
make run
```